

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	510	703/14.ccls.	USPAT	OR	OFF	2004/11/15 14:50
L2	470	1 and simulat\$3	USPAT	OR	OFF	2004/11/15 14:51
L3	254	2 and configur\$5	USPAT	OR	OFF	2004/11/15 14:51
L4	94	3 and event	USPAT	OR	OFF	2004/11/15 14:51
L5	54	4 and object	USPAT	OR	OFF	2004/11/15 14:51
L6	48	5 and signal	USPAT	OR	OFF	2004/11/15 14:52
L7	8	6 and FPGA	USPAT	OR	OFF	2004/11/15 15:03
L8	448	716/11.ccls.	USPAT	OR	OFF	2004/11/15 15:03
L9	216	8 and simulat\$3	USPAT	OR	OFF	2004/11/15 15:04
L10	16	9 and FPGA	USPAT	OR	OFF	2004/11/15 15:04
L11	5	10 and event	USPAT	OR	OFF	2004/11/15 15:04
L13	5	11 and object	USPAT	OR	OFF	2004/11/15 15:04
L14	4	13 and signal	USPAT	OR	OFF	2004/11/15 15:07
L15	2209	FPGA and simulat\$3	US-PGPUB; USPAT	OR	OFF	2004/11/15 15:07
L16	1871	15 and configur\$5	US-PGPUB; USPAT	OR	OFF	2004/11/15 15:07
L17	816	16 and event	US-PGPUB; USPAT	OR	OFF	2004/11/15 15:08
L18	477	17 and object	US-PGPUB; USPAT	OR	OFF	2004/11/15 15:08
L19	457	18 and signal	US-PGPUB; USPAT	OR	OFF	2004/11/15 15:08
L20	1069	FPGA and simulat\$3	USPAT	OR	OFF	2004/11/15 15:08
L21	910	20 and configur\$5	USPAT	OR	OFF	2004/11/15 15:09
L22	368	21 and event	USPAT	OR	OFF	2004/11/15 15:09
L23	202	22 and object	USPAT	OR	OFF	2004/11/15 15:09
L24	191	23 and signal	USPAT	OR	OFF	2004/11/15 15:09
L25	35	24 and ((look adj up adj table) or LUT)	USPAT	OR	OFF	2004/11/15 15:09
L26	29	25 and delay	USPAT	OR	OFF	2004/11/15 15:09
L27	2	26 and LIFO	USPAT	OR	OFF	2004/11/15 15:10
S1	141	703/19.ccls.	USPAT	OR	OFF	2004/11/15 14:49
S2	0	703/19.ccls. and (local adj transformation)	USPAT	OR	OFF	2004/05/13 14:07
S3	28	(703/19.ccls. and delay) and reduction	USPAT	OR	OFF	2004/05/13 14:08
S4	118	703/19.ccls. and delay	USPAT	OR	OFF	2004/05/13 14:09

S5	54	(703/19.ccls. and delay) and (timing adj analysis)	USPAT	OR	OFF	2004/05/13 14:56
S6	5	703/19.ccls. and (timing adj optimization)	USPAT	OR	OFF	2004/05/13 15:00
S7	4754	(timing adj optimization)or (iterative adj refinement) or (logic adj resynthesis) or (timing adj resynthesis) or (synthesis adj techniques) or (logic adj optimization) or (delay adj reduction) or (local adj transformation)	USPAT	OR	OFF	2004/05/13 15:04
S8	0	((timing adj optimization)or (iterative adj refinement) or (logic adj resynthesis) or (timing adj resynthesis) or (synthesis adj techniques) or (logic adj optimization) or (delay adj reduction) or (local adj transformation)) and (fanin and decomposition and collapse)	USPAT	OR	OFF	2004/05/13 15:04
S9	647	((timing adj optimization)or (iterative adj refinement) or (logic adj resynthesis) or (timing adj resynthesis) or (synthesis adj techniques) or (logic adj optimization) or (delay adj reduction) or (local adj transformation)) and (fanin or decomposition or collapse)	USPAT	OR	OFF	2004/05/13 15:04
S10	30	((((timing adj optimization)or (iterative adj refinement) or (logic adj resynthesis) or (timing adj resynthesis) or (synthesis adj techniques) or (logic adj optimization) or (delay adj reduction) or (local adj transformation)) and (fanin or decomposition or collapse)) and (critical adj path)	USPAT	OR	OFF	2004/05/13 15:05